

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,282,756 B2
APPLICATION NO. : 10/665151
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INVENTOR(S) : Agarwal et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 10, line 52, in Claim 10, delete "of the;" and insert
-- of the microstructures; --, therefor.

In column 12, lines 16-17, in Claim 15, delete "semiconductor;" and insert
-- semiconductor substrate; --, therefor.

Signed and Sealed this

Twenty-fifth Day of December, 2007

A handwritten signature in black ink, appearing to read "Jon W. Dudas". The signature is stylized with a large, looped initial "J" and a cursive "Dudas".

JON W. DUDAS
Director of the United States Patent and Trademark Office